

TESTA 2017 – 2nd Test Standards Application Workshop

ETS 2017 Fringe Workshop, May 25-26, 2017, Limassol, Cyprus, <http://www.iti.uni-stuttgart.de/testa2017/>

Early registration by **April 14** through the [ETS website](#).

Preliminary Program

Thursday, May 25

16:30-16:45 **Welcome**, A. Jutman, M. Portolan, R. Krenz-Baath

16:45-17:45 Keynote *tbd.*

18:00-20:00 **Wine and Cheese Panel** jointly with the RESCUE workshop

- (Tentative!) **Impact of standards to systems quality, reliability and security**
Experts from Robert Bosch, IROC, Cadence, and more. Moderated by Erik Larsson, Uni Lund, SW.

20:00-21:30 Fringe Workshops Reception with a light buffet dinner

Friday, May 26

9:00-10:00 **Embedded Tutorial 1** - Emerging Standards for Analog Test Access and Fault Modeling,
Stephen Sunter

10:00-10:30 Coffee

10:30-12:30 **Technical Session 1**

- Modeling a 'data ready' observation port mechanism within an hierarchical IJTAG methodology, *Martin Keim, Luc Romain, Reinhard Meier, Benoit Nadeau-Dostie and Albert Au*
- Bridging the IEEE 1687 gap between DfT and ATE, *Michele Portolan*
- Fault Extraction in Self-Reconfiguring IEEE 1687 Networks, *Farrokh Ghani Zadegan, Dimitar Nikolov and Erik Larsson*
- Enabling Ecosystem for ITC'2016 IEEE 1687 Benchmark Networks, *Artur Jutman, Anton Tsertov and Sergei Devadze*

12:30-13:30 Lunch

13:30-15:00 **Technical Session 2**

- Quantifying Security in Reconfigurable Scan Networks, *Laura Rodríguez Gómez, Michael Kochte, Ahmed Atteya and Hans-Joachim Wunderlich*
- Reducing Pessimism in Upper-Bound Computation for Optimal IEEE 1687 Retargeting, *Farrokh Ghani Zadegan, Rene Krenz-Baath and Erik Larsson*
- So, your IJTAG network fails ... now what?, *Martin Keim, Givargis Danialy, Geir Eide and Matthew Knowles*

15:00-15:30 Coffee

15:30-16:30 **Embedded Tutorial 2** - IEEE1500: its adoption and use in the industry, *Steven Oostdijk, Dave Johnson and Yervant Zorian*